

**REMARKS**

Reconsideration of dependent claims 2, 12, and 22 is respectfully requested. Each of these claims call for partitioning a global method lookup table into smaller and distributed versions for said local memory sub-region. The office action contends that local memory sub-regions are used in the cited reference. Without accepting this proposition, even if it is so, there is not both a global method lookup table and a smaller and distributed version for a local memory sub-region.

Referring to the cited Shaylor reference, the rejection of claim 2 points to the method table 216 and the field table 220. Apparently, the suggestion is that one of these constitutes the global method lookup table and one of them constitutes the smaller and distributed version for a local memory sub-region. But the description of these two tables is explicit that they both relate to the same class. Thus, the granularity does not work. That is, both of these tables apply to the same granularity: a class. Only one table relates to methods, namely, the method table 216 and the field table 220 has no relationship to the claimed invention.

In column 4, lines 35-40, the method table 216 is described as including pointers to the actual byte codes that implement the methods defined within "the particular class." The field table is described at column 4, lines 40-44, as including the values of fields associated with "the particular class." Thus, it is respectfully submitted there are not two granularities in the cited reference, one of which is global and one of which is directed to particular distributed versions of local memory sub-regions. Thus, the limitations of claim 2 were not met.

Moreover, the cited reference does not teach using both global and sub-region specific method lookup tables. It is clear beyond doubt that the reference only has one memory lookup table and that memory lookup table can be contended by the Examiner to be either a local memory table or a global memory table, but not both.

Therefore, reconsideration is respectfully requested.

Respectfully submitted,

Date: January 9, 2008



---

Timothy N. Trop, Reg. No. 28,994  
TROP, PRUNER & HU, P.C.  
1616 South Voss Road, Suite 750  
Houston, TX 77057-2631  
713/468-8880 [Phone]  
713/468-8883 [Fax]

Attorneys for Intel Corporation